Lower Bounds on Circuit Complexity

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Estonian Theory Days 02 October 2009

- inputs: propositional variables x₁, x₂,..., x_n and constants 0, 1
- gates: binary functions
- fan-out of a gate is unbounded



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• For $t = 2^n/(10n)$, F(n, t) is approximately $2^{2^n/5}$, which is $\ll 2^{2^n}$.

• Thus, the circuit complexity of almost all Boolean functions on *n* variables is exponential in *n*. Still, we do not know any explicit function with super-linear circuit complexity.

Known Lower Bounds

	circuit size	formula size
full binary basis B_2	3n-o(n)	$n^{2-o(1)}$
	[Blum]	[Nechiporuk]
basis $U_2 = B_2 \setminus \{\oplus, \equiv\}$	5n - o(n)	$n^{3-o(1)}$
	[lwama et al.]	[Hastad]
	exponential	
monotone basis $M_2 = \{\lor, \land\}$	[Razborov; Alon, Boppana;	
	Andreev; Karchmer, Wigderson]	

Explicit Functions

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- Not explicitly defined function of high circuit complexity: enumerate all Boolean functions on n variables and take the first with circuit complexity at least 2ⁿ/(10n).
- To avoid tricks like this one, we say that a function f is explicitly defined if $f^{-1}(1)$ is in NP.
- Usually, under a Boolean function f we actually understand an infinite sequence {f_n | n = 1, 2, ...}.

Known Lower Bounds for Circuits over B_2

Known Lower Bounds

 $\begin{array}{ll} 2n-c & [Schnorr, 74] \\ 2.5n-o(n) & [Paul, 77] \\ 2.5n-c & [Stockmeyer, 77] \\ 3n-o(n) & [Blum, 84] \end{array}$

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This Talk

In this talk, we will present a proof of a 7n/3 - c lower bound which is as simple as Schnorr's proof of 2n - c lower bound.

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Gate Elimination

All the proofs are based on the so-called gate elimination method. This is essentially the only known method for proving lower bounds on circuit complexity.

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Remark

This method is very unlikely to produce non-linear lower bounds.























The Class $Q_{2,3}^n$

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- Then $\operatorname{MOD}_{3,r}^n, \operatorname{MOD}_{4,r}^n \in Q_{2,3}^n$, but $\operatorname{MOD}_{2,r}^n \notin Q_{2,3}^n$.

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Remark

Optimal circuits contain AND- and XOR-type gates only, as constant and degenerate gates can be easily eliminated.

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- That is why, in particular, the current record bounds for circuits over *U*₂ = *B*₂ \ {⊕, ≡} are stronger than the bounds over *B*₂.
- Usually, the main bottleneck of a proof based on gate elimination is a circuit whose top contains many XOR-type gates.

Polynomials over GF(2)

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Lemma (Degree lower bound)

Any circuit computing f contains at least $deg(\tau(f)) - 1$ AND-type gates.

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Definition

For a circuit C, let A(C) and X(C) denote the number of AND- and XOR-type gates in C, respectively. Let also $\mu(C) = 3X(C) + 2A(C)$.

Lemma

For any circuit C computing $f \in Q_{2,3}^n$, $\mu(C) = 3X(C) + 2A(C) \ge 6n - 24$.

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 In both cases, we can assign x_i a constant such that μ is reduced at least by 6.
Lemma

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$$\frac{3X(C) + 2A(C) \ge 6n - 24}{A(C) \ge n - c - 1}$$
$$\frac{A(C) \ge n - c - 1}{C(f) = 3X(C) + 3A(C) \ge 7n - 25 - c}$$

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 - No, there is a circuit computing MOD₃ⁿ of size 3n containing exactly n AND-type gates.
 - Moreover, any symmetric function can be computed using only *n* AND-type gates.
 - No lower bound better than n-1 is known! Though the multiplicative complexity of almost all functions is exponential.

MOD_3 vs MOD_4

MOD_3 is not simpler than MOD_4

For circuits and formulas over B_2 and U_2 , it is known that MOD_3 is not simpler than MOD_4 . The exact complexity of MOD_4 is known for some of these models: $C_{B_2}(MOD_4^n) = 2.5n - c$, $L_{B_2}(MOD_4^n) = \Theta(n \log n)$. The exact complexity of MOD_3 is known for none of these models.

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- MOD₃ survives under substitutions like $x_i = x_j$.
- $C_{B_2}(MOD_3^n)$ for $n \le 5$ "grows like" 3n.

Open Problems

Close the gaps:

 $2.5n \le C_{B_2}(\text{MOD}_3^n) \le 3n$ $4n \le C_{U_2}(\text{MOD}_4^n) \le 5n$

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2 Prove a *cn* lower bound (for a constant c > 1) on the multiplicative complexity of an explicit Boolean function.

Thank you for your attention!