# Unified Static and Runtime Verification of Object-Oriented Software

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#### Static Verification vs. Runtime Verification

- Static verification
  - ▶ High precision
  - Use abstractions for increased automation

#### but

- Powerful judgements hard to achieve automatically
- Often losing aspects of concrete system
- Runtime verification
  - ► Full precision (including real deployment)
  - Full automation

#### but

- Cannot judge future runs
- Computational overhead of monitoring the running system

## Project on Unified Static and Runtime Verification

Unified Static and Runtime Verification of Object-Oriented SW

#### Members:

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   Chalmers University of Technology
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- Gerardo Schneider,
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#### External collaborator:

Gordon J. Pace,
 University of Malta



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#### Framework for Unified Static and Runtime Verification

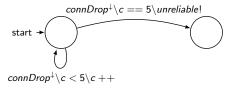
- Combine static and runtime verification
  - ► Combine data centric and control centric properties
  - Unified specification for both
- Use (partial) static verification results for partial evaluation of properties
- Runtime verification of resulting properties
- ► Increase safety and efficiency

#### LARVA: A Runtime Verification Tool for Java

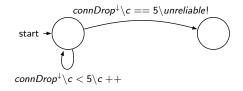
#### LARVA≡ Logical Automata for Runtime Verification and Analysis

- targets Java applications
- checks control oriented properties (untimed and real-time), specified in
  - ► DATE (Dynamic Automata with Timers and Events)
  - Lustre
  - duration calculus

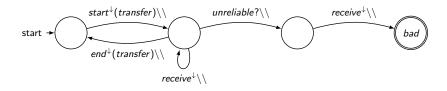
#### DATE Automaton Example



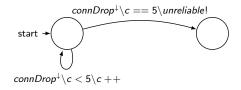
### DATE Automaton Example



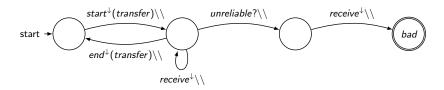
#### foreach transfer:



### **DATE** Automaton Example



#### foreach transfer:



#### In general:

- communicating automata, event-triggered transitions, timers
- events: method entry/exit, timer events, synchronising events

# LARVA Functionality

- ► LARVA input
  - ► DATE automaton (or alternative format)
  - ► application code

# LARVA Functionality

- ► LARVA input
  - DATE automaton (or alternative format)
  - application code
- ► LARVA output
  - monitor
  - instrumented application code, with triggers for monotor

#### KeY

KeY is an approach and tool for the

- ► Formal specification of foremost *functional* properties
- ► Deductive verification, i.e., using theorem proving

of

▶ OO software, foremost JAVA and ABS

#### KeY

- Dynamic logic (generalisation of Hoare logic) as program logic
- ► Verification = symbolic execution + induction/invariants
- Sequent calculus
- ▶ Prover is automated + interactive
- most elaborate KeY instance KeY-Java
  - Java as target language
  - Supports specification language JML

# Specification Language for Data and Control

#### ppDATE:

Extending DATE with pre/post-conditions, associated to the automata's states:

Transition enabled if cond holds

*ppDATE* trace  $w \in (\Sigma^{\updownarrow} \times \Theta)^*$  is violating prefix if either

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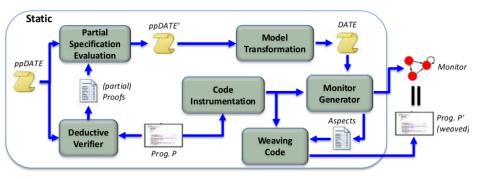
- ▶  $(q_0, v_0) \stackrel{w}{\Rightarrow} (q, v)$  and  $q \in BadStates$
- $w = w_1 + \langle (m_{id}^{\downarrow}, \theta_1) \rangle + w_2 + \langle (m_{id}^{\uparrow}, \theta_2) \rangle$  such that:
  - 1.  $(q_0, v_0) \stackrel{w_1}{\Longrightarrow} (q, v)$
  - 2.  $\tau(q) \ni \{pre\} m \{post\}$
  - 3.  $\theta_1 \models pre$
  - 4.  $\theta_2 \not\models post$

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A violating trace has a violating prefix

### High-level description of the framework

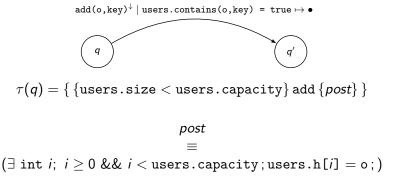


Case study: Login Example

#### Scenario:

- ▶ At login, new users are added to set users
- Assume users is implemented using hashing with open addressing
- Adding implemented by users.add(u,key)

### Case study: Login Example



## Case study: Login Example - Static Analysis

Translation of Hoare triple to JML

```
class HashTable {
 . . .
/*@ public normal_behavior
  @ requires size < capacity;</pre>
  @ ensures (\exists int i:
  0
                       i>= 0 && i < capacity;
  0
                       h[i] == o):
  @ assignable size, h[*];
  . . . . . .
  0*/
public void add (Object o, int key) {}
```

# Case study: Login Example - Static Analysis

```
public void add (Object o, int key) {
    ...
    int i = hash(key);
    if (h[i] == null) {
        h[i] = o; size++;
    }
    else {
        while ... \\ store at next free slot
        ...}
}
```

# Case study: Login Example - Static Analysis

- ► KeY tries to prove: size < capacity → (add(o, key)) ∃ i. h[i] = o</p>
- ► KeY will produce branches:
  ...,h[key%capacity] = null ⊢ ...
  and
  ...,¬h[key%capacity] = null ⊢ ...
- ▶ first branch closes automatically, the second doesn't

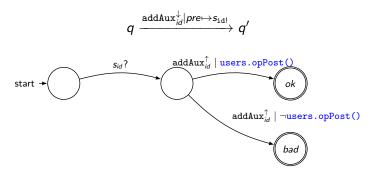
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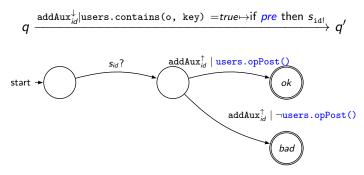
Second, new argument is added to distinguish different calls

```
Second, new argument is added to distinguish different calls
  public void add (Object o, int key) {
     addAux(fid.getNewId(),o,key);
  public void addAux (Integer id, Object o, int key) {
    //same code as add had before.
  {pre ∧ ¬users.h[key%capacity] = null} addAux {post}
                           and
   {pre \( \) users.h[key\( \)capacity] = null\) addAux\{true\}
```

### Case study: Login Example - Model Transformation



#### Case study: Login Example - Model Transformation



 $\frac{\text{addAux}_{id}^{\downarrow} | \neg (\text{users.contains(o, key}) = \text{true}) \land (\textit{pre} \land \neg \text{users.h[key%capacity]} = \text{null}) \mapsto s_{\text{idl}} }{\rightarrow}$ 

## Case study: Login Example - Monitor Generation

Finally, LARVA generates the monitors which will control the partially verified property.

#### Reference

- Wolfgang Ahrendt, Gordon J. Pace, Gerardo Schneider A Unified Approach for Static and Runtime Verification:
  - Framework and ApplicationsISoLA 2012

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